

FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE
STATEMENT BY APPLICANTS
(37 CFR 1.98(b))Attorney Docket No.:
MUH-11086Applic. No.
09/931,689

Applicants

Wolfgang Werner

Filing Date
August 16, 2001Group Art Unit
0.0

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
DF	A	5,801,420	09/98	Fujishima			
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
DF	J	0 338 312 A2	10/89	Europe			X
DF	K	0 273 030 A2	06/88	Europe			X
DF	L	0 420 672 A1	04/91	Europe			X
DF	M	0 767 499 A2	04/97	Europe			X
DF	N	0 915 508 A1	05/99	Europe			X

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

DF	O	Disney et al.: "SOI LIGBT Devices with a Dual P-Well Implant for Improved Latching Characteristics" 5 th International Symposium on Power Semiconductor Devices and ICs, 1993 IEEE, pages 254 - 258
DF	P	Udrea et al.: "Lateral Insulated Gate Bipolar Transistor (LIGBT) Structure Based on Partial Isolation SOI Technology" Electronics Letters, May 8, 1997, Vol. 33, No. 10, pages 907 - 909

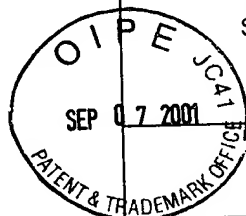
EXAMINER

Da Shin

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6/20/02

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicants.



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		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO
OF	J	148 546	05/81	East Germany			X
OF	K	198 05 786 A1	08/99	Germany			X
OF	L	37 25 429 A1	02/89	Germany			X
OF	M	1 564 547	05/70	Germany			X
OF	N	03245562	11/91	Japan			X

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

OF	O	Simpson et al.: "Analysis of the Lateral Insulated Gate Transistor", IEDM 85, 1985 IEEE, pages 740 - 743.
	P	

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DF	J	63081861	04/88	Japan			X
	K						
	L						
	M						
	N						

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